



ANALOG MODULES, INC.

Specialists in Analog and Laser Electronics

5723/5724 and 5753/5754

CONTROL INTERFACE DESCRIPTION

(Standard Configuration for use with 57XX-XXXX-1-X Series Modules)

PIN	SIGNAL NAME	DESCRIPTION
1	TEMPERATURE TEST POINT	Represents charger temperature as a DC voltage through 4.7k Ω of output impedance (reference Figure 1). Refer to temperature test point data chart. Shutdown occurs at approximately 72°C.
2	PROGRAM VOLTAGE	0 to 10V control differential input for 0 to 100% rated output voltage (reference Figure 2). Models 5753 and 5754 require driving two circuits connected in parallel.
3		NO CONNECTION
4	PRIMARY INHIBIT	3.5 to 30V input to inhibit charger. 10k Ω load impedance. (reference Figure 3).
5		NO CONNECTION
6	+5 VOLT REFERENCE	5V reference with 100 Ω source impedance. 10mA maximum current draw. Overload on this line could interfere with normal charger operation (reference Figure 5).
7	OVERTEMP FAULT INDICATOR	Open collector output rated to 16V and capable of sinking up to 15mA. Fault indicated by low condition (reference Figure 6).
8	END OF CHARGE	Diode isolated output of 15.5V capable of sourcing up to 15mA. Charge complete indicated by high output signal (reference Figure 4).
9	PROGRAM RETURN	0 to 10V control differential input return (reference Figure 2).
10	SIGNAL RETURN	Used for low current signal output, and input returns.
11		NO CONNECTION
12		NO CONNECTION
13		NO CONNECTION
14		NO CONNECTION
15		NO CONNECTION

TEMPERATURE TEST POINT DATA

TEMPERATURE IN CENTIGRADE	TEST POINT VOLTAGE	TEMPERATURE IN CENTIGRADE	TEST POINT VOLTAGE
25°	5.28V	50°	7.23V
26°	5.38V	51°	7.29V
27°	5.48V	52°	7.34V
28°	5.57V	53°	7.39V
29°	5.66V	54°	7.44V
30°	5.76V	55°	7.49V
31°	5.85V	56°	7.55V
32°	5.94V	57°	7.58V
33°	6.03V	58°	7.62V
34°	6.11V	59°	7.67V
35°	6.20V	60°	7.71V
36°	6.29V	61°	7.75V
37°	6.36V	62°	7.79V
38°	6.44V	63°	7.82V
39°	6.51V	64°	7.85V
40°	6.59V	65°	7.89V
41°	6.67V	66°	7.92V
42°	6.74V	67°	7.95V
43°	6.81V	68°	7.99V
44°	6.88V	69°	8.02V
45°	6.94V	70°	8.05V
46°	7.00V	71°	8.07V
47°	7.06V	72°	8.10V
48°	7.12V	73°	8.13V
49°	7.17V	74°	8.15V

5723/5724/5723/5754 INTERFACE CIRCUITS
(STANDARD INTERFACE)

FIG. 1 TEMPERATURE TEST POINT

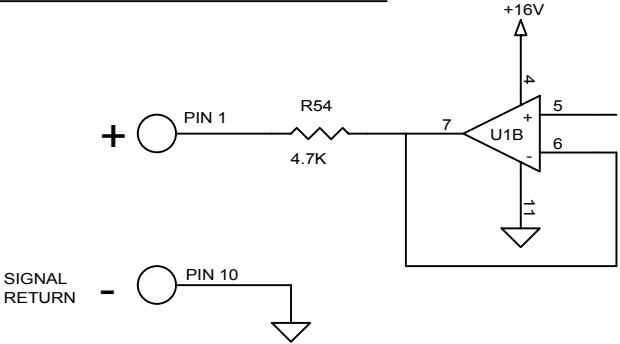
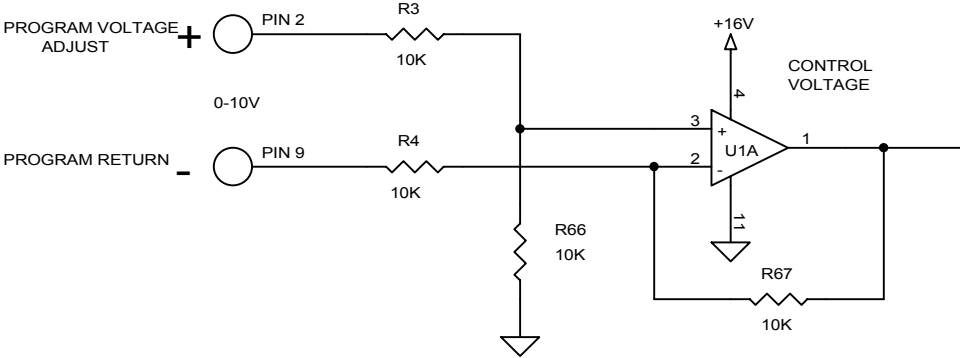
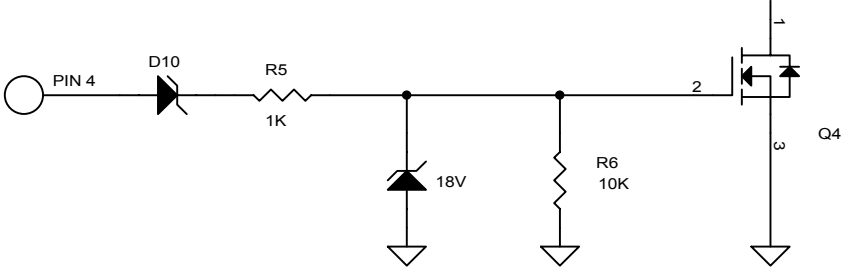


FIG. 2 PROGRAM VOLTAGE



0-10V CONTROL R66,67 = 10K
0-5V CONTROL R66,67 = 20K

FIG. 3 INHIBIT



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5723/5724/5723/5754 INTERFACE CIRCUITS (STANDARD INTERFACE)

FIG. 4 END OF CHARGE

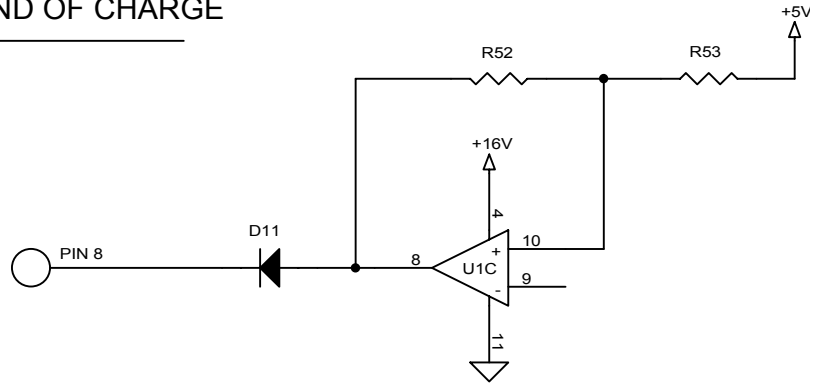


FIG. 5 +5V REFERENCE

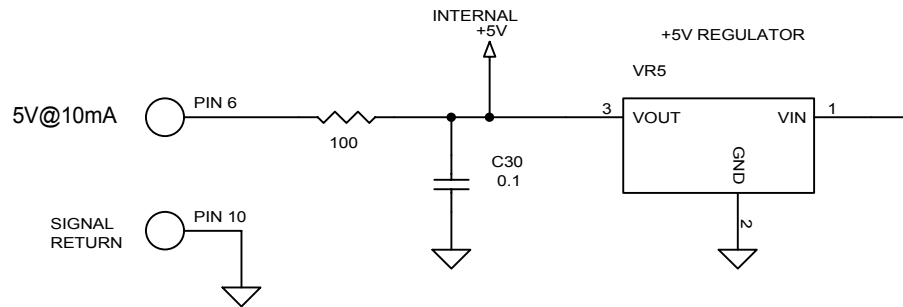
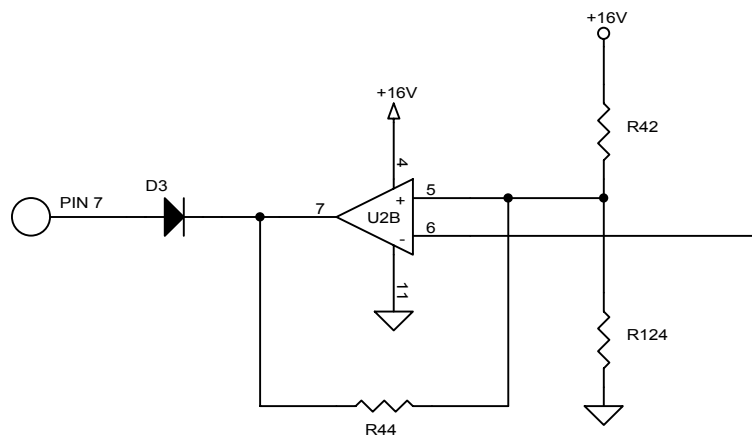


FIG. 6 OVERTEMP STATUS



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