

ANALOG MODULES, INC.

Specialists in Analog and Laser Electronics

MODEL 7701A INTERFACE DESCRIPTION

PIN		SIGNAL NAME	DESCRIPTION
	1	ENABLE IN HIGH	Input, + 24V @ 10mA to enable control pulses to the output. Optically isolated.
14		ACTIVATE CROWBAR	Input, +5 to +15 VDC activates the crowbar, 2 k Ω load impedance.
	2	GROUND	
15		NOT CONNECTED	
	3	CURRENT MONITOR OUT	Output, 0 to 10 volts yields 0 to max output current @10 mA typical.
16		CROWBAR OUT	Output, latched signal indicates crowbar tripped, active high, 15 volts, output impedance 1 $k\Omega$.
	4	GROUND	
17		NOT CONNECTED	
	5	PULSE IN HIGH	Input, +5 volts @ 10 mA. Optically isolated with an internal 300Ω resistor.
18		NOT CONNECTED	
	6	GROUND	
19		MODE IN LOW	Input, +24 volts @ 7.2 mA. Optically isolated signal with an internal 32 $k\Omega$ pull low for CW mode.
	7	CURRENT CONTROL IN	Input, 0 to 10VDC yields 0 to max output current (50 k Ω impedance)
20		DIFF. VOLTAGE OUT	Output, Impedance isolated pulsed signal, 0 to 10V yields 0 to 20V across the driver.
	8	GROUND	
21		NOT CONNECTED	
	9	PULSE IN LOW	Input, +5 volts @ 10mA. Optically isolated signal, active low, pulsewidth determines output pulsewidth.
22		NO LOAD SIGNAL	Output, 100Ω output impedance. Pull low when no load condition is detected. 15V indicates normal operation.
	10	GROUND	
23		NOT CONNECTED	
	11	NOT CONNECTED	
24		GROUND	
	12	VOLTAGE MONITOR OUT	Output, Impedance isolated pulsed signal, 0 to 10VDC yields 0 to 300V across the load.
25		NOT CONNECTED	
	13	NOT CONNECTED	