5705 CONTROL INTERFACE DESCRIPTION

PIN	SIGNAL NAME	DESCRIPTION
1	TEMPERATURE TEST POINT	Represents charger temperature as a DC voltage through 1.0kΩ of output impedance (reference Figure 1). Refer to temperature test point data chart. Shutdown occurs at approximately 72°C.
2	PROGRAM RETURN	0 to 10V control differential input return (reference Figure 2).
3	PROGRAM VOLTAGE	0 to 10V control differential input (reference Figure 2).
4	SIGNAL RETURN	Used for low current signal output, and input returns.
5 & 6	24V RETURN	Main 24V power return.
7	PRIMARY INHIBIT	3.5 to 30V input to inhibit charger. 10kΩ load impedance (reference Figure 3).
8	POWER CONTROL	Consult factory
9 & 10	24 VOLT INPUT	24V at 250mA required to power control board.
11	+10 VOLT REFERENCE	10V reference with 100Ω source impedance. 2mA maximum current draw. (Reference Figure 4.)
12	CURRENT MONITOR	0-10VDC @ 2mA (max.) represents 0-Full. Scale output current. 1000Ω source impedance. (Reference Figure 5.) Available with -I units only.
13	OVERTEMP OUT (16V Maximum)	Open collector output rated to 15V and capable of sinking up to 10mA (reference Figure 6).
14	VOLTAGE MONITOR	0-10VDC @ 2mA (max.) represents 0-full scale output voltage. 1000Ω source impedance (reference figure 6).
15	MASTER/SLAVE PIN	Must be shorted to Pin 16 on voltage mode units for normal option.
16	MASTER/SLAVE PIN	Must be shorted to pin 15 on voltage mode units for normal operation.

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TEMPERATURE TEST POINT DATA

TEMPERATURE IN CENTIGRADE	TEST POINT VOLTAGE	TEMPERATURE IN CENTIGRADE	TEST POINT VOLTAGE
25°	5.28V	50°	7.23V
26°	5.38V	51°	7.29V
27°	5.48V	52°	7.34V
28°	5.57V	53°	7.39V
29°	5.66V	54°	7.44V
30°	5.76V	55°	7.49V
31°	5.85V	56°	7.55V
32°	5.94V	57°	7.58V
33°	6.03V	58°	7.62V
34°	6.11V	59°	7.67V
35°	6.20V	60°	7.71V
36°	6.29V	61°	7.75V
37°	6.36V	62°	7.79V
38°	6.44V	63°	7.82V
39°	6.51V	64°	7.85V
40°	6.59V	65°	7.89V
41°	6.67V	66°	7.92V
42°	6.74V	67°	7.95V
43°	6.81V	68°	7.99V
44°	6.88V	69°	8.02V
45°	6.94V	70°	8.05V
46°	7.00V	71°	8.07V
47°	7.06V	72°	8.10V
48°	7.12V	73°	8.13V
49°	7.17V	74°	8.15V

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INTERFACE CIRCUITS

FIG. 1 TEMPERATURE TEST POINT

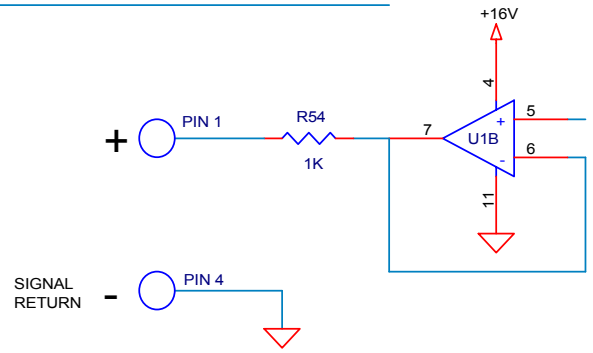
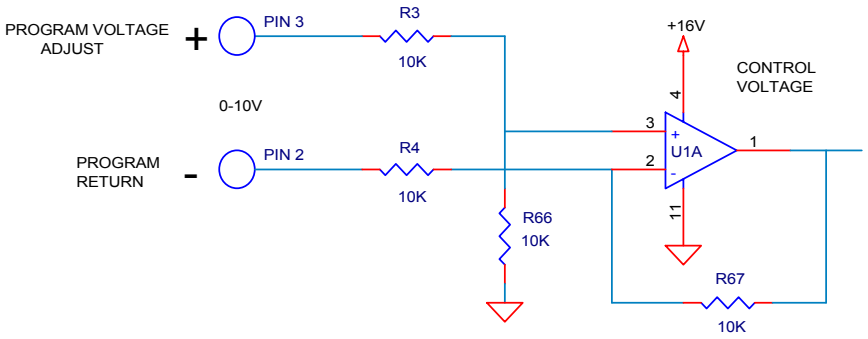


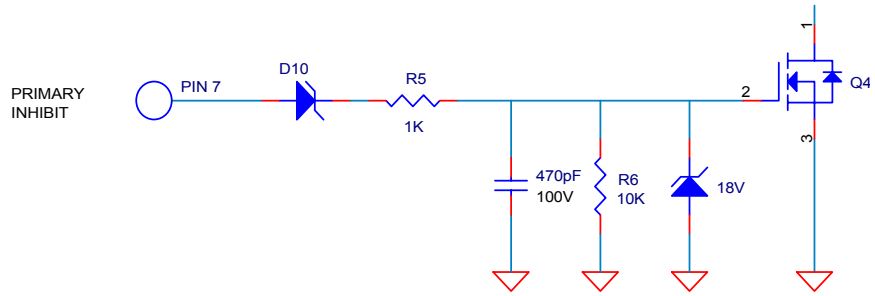
FIG. 2 PROGRAM VOLTAGE



0 - 10V CONTROL R66,67 = 10K

0 - 5V CONTROL R66,67 = 20K

FIG. 3 INHIBIT / END OF CHARGE



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INTERFACE CIRCUITS

FIG. 4 +10V REFERENCE

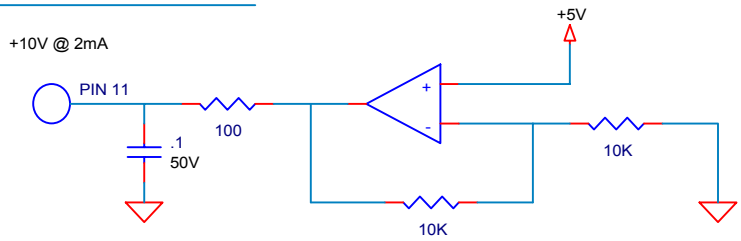


FIG. 5 CURRENT MONITOR

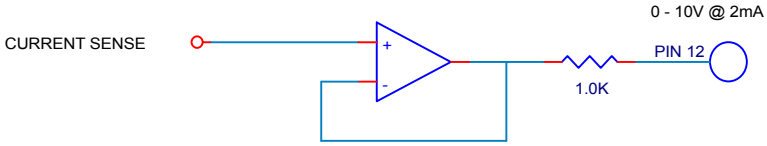


FIG. 6 OVER TEMP OUTPUT

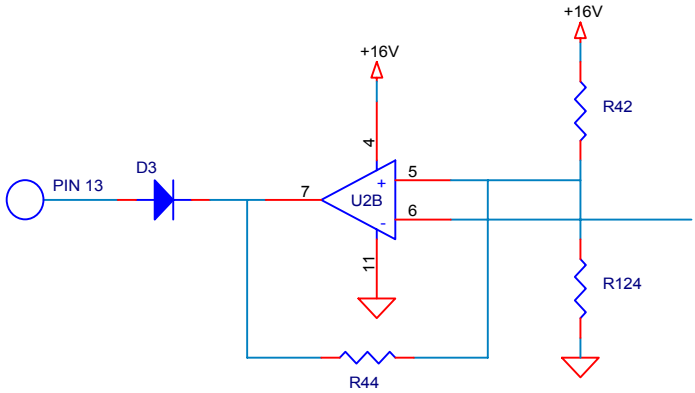
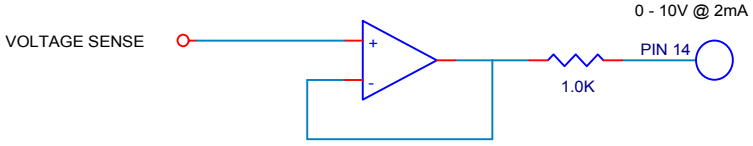


FIG. 7 VOLTAGE MONITOR



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